

ISTITUTO NAZIONALE DI RICERCA METROLOGICA Repository Istituzionale

Trilateral Comparison Among Digital and Josephson Impedance Bridges

This is the author's submitted version of the contribution published as:

Original

Trilateral Comparison Among Digital and Josephson Impedance Bridges / Marzano, Martina; Ortolano, Massimo; Overney, Frédéric; Eichenberger, Ali L.; Kucera, Jan; D'Elia, Vincenzo; Bartova, Lucie; Medved, Juan; Callegaro, Luca. - (2024), pp. 1-2. (Intervento presentato al convegno 2024 Conference on Precision Electromagnetic Measurements (CPEM) tenutosi a Denver (US) nel 8-12 Luglio 2024) [10.1109/cpem61406.2024.10646159].

Availability:

This version is available at: 11696/82247.3 since: 2025-01-27T13:00:05Z

Publisher:

Published

DOI:10.1109/cpem61406.2024.10646159

Terms of use:

This article is made available under terms and conditions as specified in the corresponding bibliographic description in the repository

Publisher copyright

IEEE

© 20XX IEEE. Personal use of this material is permitted. Permission from IEEE must be obtained for all other uses, in any current or future media, including reprinting/republishing this material for advertising or promotional purposes, creating new collective works, for resale or redistribution to servers or lists, or reuse of any copyrighted component of this work in other works

(Article begins on next page)

Trilateral Comparison Among Digital and Josephson Impedance Bridges

Martina Marzano¹, Massimo Ortolano², Frédéric Overney³, Ali L. Eichenberger³, Jan Kucera⁴, Vincenzo D'Elia¹,
Lucie Bartova⁴, Juan Medved^{1, 2}, and Luca Callegaro¹

¹Istituto Nazionale di Ricerca Metrologica INRIM, Torino, Italy

²Politecnico di Torino POLITO, Torino, Italy

massimo.ortolano@polito.it

³Federal Institute of Metrology METAS, Bern, Switzerland

⁴Czech Metrology Institute CMI, Brno, Czech Rebublic

Abstract—In this work, we present the results of an onsite trilateral comparison between a dual Josephson impedance bridge developed by METAS and the electronic fully-digital impedance bridges developed by CMI and INRIM-POLITO. The target accuracies of the bridges are at the 10^{-8} level for the former and at the 10^{-7} for the latter. Here we report the results of the calibration of a $10\,\mathrm{nF}$ capacitance standard against a $12.9\,\mathrm{k}\Omega$ calculable resistance standard at $1233\,\mathrm{Hz}$, conditions suitable for the primary direct realization of the impedance units ohm and farad from AC quantum Hall resistance standards or from AC/DC calculable transfer resistance standards calibrated against DC quantum Hall resistance standards.

Index Terms—Metrology, calibration, impedance measurement, bridge circuits, Josephson effect

I. INTRODUCTION

Dual Josephson impedance bridges (DJIBs) and electronic fully-digital impedance bridges (FDIBs) have emerged in recent years as devices suitable for the primary direct realization of the impedance units ohm and farad from AC quantum Hall resistance (QHR) standards or from AC/DC calculable transfer resistance standards calibrated against DC QHR standards. The target accuracy for DJIBS is at the 10^{-9} to 10^{-8} level, whereas that for FDIBs is at the 10^{-8} to 10^{-7} level. In 2020, INRIM, METAS, PTB and POLITO started the project EURAMET TC-EM 1501, *Technical assessment of novel digital impedance bridges* [1], with the aim of performing comparisons among impedance bridges under development. The project is being coordinated by INRIM, and CMI joined it at the end of 2023.

A first round of comparisons took place from September to November 2021 when the INRIM-POLITO FDIB was moved to Braunschweig to be compared with the PTB DJIB. The results of this first round were published in [2]. A second round of comparisons took place in 2023 in Bern, where the INRIM-POLITO FDIB (from October to December) and the CMI FDIB (mid October) were moved to and compared with the METAS DJIB.

II. PARTICIPATING IMPEDANCE BRIDGES

An impedance bridge determines the ratio of two impedance standards. The DJIB and the two FDIBs participating in this comparison are four-terminal-pair (4TP) sourcing bridges, where the impedance ratio is determined from a reference voltage ratio generated by a multiphase two-channel source, and the impedance standards are defined as 4TP standards [3]. In a DJIB, the source is a dual Josephson arbitrary waveform

synthesizer (JAWS); in a FDIB, the source is an electronic digital synthesizer. These bridges can compare like impedances (e.g. R:R or C:C) or unlike impedances (e.g. R:C or R:L), determining magnitude and argument (phase) of the ratio. The 4TP definition of the impedances is realized by a number of auxiliary sources, injection and detection transformers, and detectors. A bridge is balanced when both the mean (main balance) and the difference (Kelvin balance) of the voltages at the low potential ports of the impedances are zero, and when the currents at the high potential ports are zero (current balances). To compensate the possible asymmetries in the bridge networks, the measurements are performed in two successive configurations, typically labelled *forward* and *reverse*, differing by the exchange of the impedances. Fig. 1 shows a picture of the three bridges in the METAS laboratory.

The CMI FDIB is based on the reconfigurable bridge [4], where the reference ratio of the bridge is formed by the ultrastable two-channel generator SWG [5]. The 4TP definition of the impedances under comparison is fulfilled by means of additional sources and injection circuit. In the 1:1 ratio mode, a two-step measurement is performed, where rebalancing in the second step is done with an additional injection circuit situated in one of potential arms. The full automation of the bridge balancing and reversing of sources is performed with the second generation of coaxial switches based on [6]. High stability of channel outputs and low crosstalk between two reference channels (both around $10^{-8} \, \text{V/V}$) ensure negligible influence of channel swapping and phase rotation of one



Fig. 1. METAS laboratory with the authors and the three installed bridges: from left to right, METAS DJIB, CMI FDIB, INRIM-POLITO FDIB.

channel on the output voltage amplitude of the second one.

The INRIM-POLITO FDIB is optimized for 1:1 ratio magnitudes, and is based on a polyphase digital source [7] with seven independently adjustable channels. Two channels provide the main voltage ratio, one channel an auxiliary injection and the other channels are used to implement the 4TP definition of the impedances. The main, Kelvin and current balances are detected by a lock-in amplifier, which is automatically switched across the various detection ports. A single complete measurement requires from about 10 min to 15 min. The operating principle of the INRIM-POLITO FDIB, its balancing procedure and an implementation without the full automation are described in detail in [8]. The bridge is now fully automated and a number of improvements in the balancing procedure have been implemented.

The METAS DJIB relies on reference voltages generated by a dual JAWS system developed by NIST. It consists of two JAWS chips mounted in separate probes, both cooled to 4.2 K within the same LHe dewar. Each JAWS chip comprises four arrays, totaling 51240 Josephson junctions (JJs) [9]. While each JAWS chip can generate an rms voltage up to 1 V at a pulse repetition of 14.4 GHz, the system operates in a 'zero-compensation' mode [10], limiting the maximum rms voltage to 0.3 V. The dual JAWS system provides reference voltages to a four-terminal-pair digitally assisted bridge developed by METAS. This bridge allows accurate measurement of impedance ratios from 1:10 to 10:1 at frequencies ranging from less than 1 kHz to 80 kHz. For a comprehensive understanding of the DJIB, including its detailed working principle, balancing procedure, and performance metrics, refer to [11].

III. RESULTS AND CONCLUSION

The comparisons of the bridges were performed at 1:1 ratio magnitude for R:R, R:C and QHR: R standards. Here we report the results for the R:C comparison at a frequency f such that $2\pi fRC\approx 1$. In the measurements there were employed a temperature-controlled $10\,\mathrm{nF}$ capacitance standard developed by GUM and SUT [12] and a $12.9\,\mathrm{k}\Omega$ quadrifilar

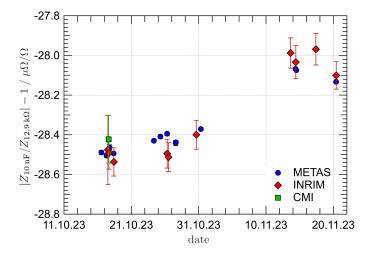


Fig. 2. Relative deviation of the amplitude component of the measured impedance ratio of $10\,nF$ and $12.9\,k\Omega,$ related to nominal ratio 1:1.

calculable resistance standard, and the working frequency is $1233.147\,\mathrm{Hz}$. This frequency is of practical interest being close to the ones typically adopted for the representation of the farad and in international comparisons. Fig. 2 shows the relative deviation from the nominal ratio 1:1 of the magnitude of the measured impedance ratio obtained with the three bridges in the campaign, with their combined standard uncertainties. Most measurements are compatible within the standard uncertainty and all are compatible within an expanded uncertainty with a coverage factor of 2. The full results for the R:R,R:C and QHR: R comparisons, both in magnitude and phase, will be presented at the conference.

ACKNOWLEDGMENTS

This work was funded by the project CAPSTAN *Quantum electrical Italian national capacitance standard* funded by the MIUR *Progetti di Ricerca di Rilevante Interesse Nazionale* (PRIN) Bando 2020, grant 2020A2M33J. It was also partly funded by Institutional Subsidy for Long-Term Conceptual Development of a Research Organization granted to the Czech Metrology Institute by the Ministry of Industry and Trade.

REFERENCES

- [1] M. Marzano, "Technical assessment of novel digital impedance bridges." [Online]. Available: https://www.euramet.org/technical-committees/tc-projects/details/project/technical-assessment-of-novel-digital-impedance-bridges
- [2] M. Marzano, Y. Pimsut, M. Kruskopf, Y. Yin, M. Kraus, V. D'Elia, L. Callegaro, M. Ortolano, S. Bauer, and R. Behr, "PTB-INRIM comparison of novel digital impedance bridges with graphene impedance quantum standards," *Metrologia*, vol. 59, p. 065001, 2022.
- [3] R. D. Cutkosky, "Four-terminal-pair networks as precision admittance and impedance standards," *IEEE Trans. Commun.*, vol. 83, pp. 19–22, 1064
- [4] J. Kučera and J. Kováč, "A reconfigurable four terminal-pair digitally assisted and fully digital impedance ratio bridge," *IEEE Trans. Instrum. Meas.*, vol. 67, no. 5, pp. 1199–1206, 2018.
- [5] J. Kučera, J. Kováč, L. Palafox, R. Behr, and L. Vojáčková, "Characterization of a precision modular sinewave generator," *Meas. Sci. Technol.*, vol. 31, p. 064002, 2020.
- [6] J. Kováč and J. Kučera, "A modular coaxial multiplexer with high isolation between channels," in *Proc. of the 21st IMEKO World Congress*, Prague, 2015, pp. 596–600.
- [7] J. Kaczmarek, M. Ortolano, O. Power, J. Kučera, L. Callegaro, V. D'Elia, M. Marzano, R. Walsh, M. Kozioł, and R. Rybski, "Virtual training laboratory for primary impedance metrology," *IEEE Trans. Instrum. Meas.*, vol. 72, pp. 1–12, 2023.
- [8] M. Marzano, M. Ortolano, V. D'Elia, A. Müller, and L. Callegaro, "A fully digital bridge towards the realization of the farad from the quantum Hall effect," *Metrologia*, vol. 58, p. 015002, 2020.
- [9] A. Rüfenacht, N. E. Flowers-Jacobs, and S. P. Benz, "Impact of the latest generation of Josephson voltage standards in ac and dc electric metrology," *Metrologia*, vol. 55, no. 5, pp. S152–S173, 2018.
- [10] S. P. Benz and S. B. Waltman, "Pulse-Bias Electronics and Techniques for a Josephson Arbitrary Waveform Synthesizer," *IEEE Trans. Appl. Supercond*, vol. 24, no. 6, pp. 1–7, dec 2014.
- [11] F. Overney, N. E. Flowers-Jacobs, B. Jeanneret, A. Rüfenacht, A. E. Fox, P. D. Dresselhaus, and S. P. Benz, "Dual Josephson impedance bridge: towards a universal bridge for impedance metrology," *Metrologia*, vol. 57, no. 6, p. 065014, dec 2020.
- [12] K. Musioł, M. Koszarny, M. Kampik, K. Kubiczek, A. Ziółek, and J. Jursza, "A New Impedance Metrology Infrastructure at GUM," in Proceedings of the 25th IMEKO TC4 International Symposium and 23rd International Workshop on ADC and DAC Modelling and Testing, vol. 21, Brescia, 2022, pp. 243–247.